

EDUCATION

B.E ELECTRONICS AND COMPUTER

Thapar Institute of Engineering and Technology | 9.76 CGPA | Patiala,Punjab | 2018 - Present

Courses: C++, Embedded Systems, Circuit Analysis and Synthesis

XII (SENIOR SECONDARY), SCIENCE

Mother Khazani Convent School | 93.2 % | Delhi | 2016 - 2018

X (SECONDARY)

St. Francis Xavier School | 10 CGPA | Hisar,Haryana | 2015 - 2016

AWARDS & HONORS

- Received Certificate of Merit for excellent performance in IICDC-2019-c200 by TI University Program
- Received Certificate of Merit for excellent performance in IICDC-2019-LIC by TI University Program
- Received Certificate of Merit for excellent performance in IICDC-2019-MSP by TI University Program
- Participated in the IICDC (India Innovation Challenge Design Contest) 2019
- Qualified NDA - 2017 (Stage 1)

PROJECTS

PROJECT PRIME

Thapar Institute of Engineering and Technology | Patiala,Punjab | Nov 2019 - Present

Project Prime is a program written in C++ that speeds up the process of primality checking on very big numbers.

Technologies : C++

PISTORE

Thapar Institute of Engineering and Technology | Patiala,Punjab | Feb 2019 - Mar 2019

Developed a circuit to store and use the electricity generated by piezoelectric.

SKILLS

C++

Embedded Systems

Linux

LANGUAGES

Hindi (Advanced)

English (Advanced)

HOBBIES



Reading



Writing

PERSONAL DETAILS

LinkedIn

<https://www.linkedin.com/in/taru-garg/>

Address

Thapar Institute of Engineering and Technology,Patiala